# Going beyond games: towards decision making in the real-world

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Meta AI (FAIR)



## Reinforcement Learning



Go



Chess



Shogi



Poker



DoTA 2



StarCraft II

#### Big Success in Games

## ELF OpenGo

#### Vs top professional players

Name (rank)	ELO (world rank)	Result
Kim Ji-seok	3590 (#3)	5-0
Shin Jin-seo	3570 (#5)	5-0
Park Yeonghun	3481 (#23)	5-0
Choi Cheolhan	3466 (#30)	5-0

Single GPU, 80k rollouts, 50 seconds Offer unlimited thinking time for the players





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[ELF OpenGo: An Analysis and Open Reimplementation of AlphaZero, Y. Tian et al, ICML 2019]

## What's Beyond Games?

## Chip Design (Google)



Several weeks with human experts in the loop

 $\rightarrow$ 

#### Fully automatic design in 6 hours

[A. Mirhoseini, A. Goldie, A graph placement methodology for fast chip design, Nature'21]

#### **Optimization Problems**



#### Wait...What?

- Many problems are NP-hard problems.
  - No good algorithm unless *P* = *NP*
- These guarantees are worst-case ones.
  - To prove a lower-bound, construct an adversarial example to fail the algorithm
- For specific distribution, there might be better heuristics.
  - Human heuristics are good but may not be suitable for everything

#### Efficient Search for Games

Go

Chess







Human Knowledge Machine learned models

#### Efficient Search for Optimization



Exhaustive search to get a good solution



#### More Efficient Search for Optimization



Exhaustive search to get a good solution

#### Components of Search

Design of State/Action Space



State Representation

**Search Heuristics** 

## Part I: Learning Search Heuristics

#### NeuroPlan: Network planning problem

A->D: 100Gbps, under several single-fiber failures



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Site

IP Link

[H. Zhu et al, Network planning with deep reinforcement learning, ACM SIGCOMM'21]

## Existing approach

ILP problem

Both  $C_l$  and  $Y(l, \omega, \lambda)$  are decision variables.

$$\begin{split} \min \sum_{l \in L} (C_l \times cost_{IP} + \sum_{f \in \Psi_l} cost_f) \quad (1) &\longleftarrow \qquad & \text{Objective} \\ \text{s.t.} \sum_{l:l_{src}=n} Y(l, \omega, \lambda) - \sum_{l:l_{dst}=n} Y(l, \omega, \lambda) = Traffic(\omega, n) \\ \forall \omega \in \Omega, \lambda \in \Lambda \qquad & (2) & \qquad & \text{Flow conservation} \\ \forall \omega \in \Omega, \lambda \in \Lambda \qquad & (2) & \qquad & \text{Constraints} \\ C_l \ge \sum_{\omega} Y(l, \omega, \lambda), \forall \lambda \in \Lambda \qquad & (3) & \longleftarrow \qquad & \text{Link capacity} \\ \sum_{l \in \Delta_f} C_l \times \phi_{lf} \le S_f \qquad & (4) & \longleftarrow \qquad & \text{Spectrum efficiency} \\ C_l \ge C_l^{min} \qquad & (5) & \longleftarrow \qquad & \text{Existing topology} \\ \end{split}$$

## Existing approach

ILP problem

$$\min \sum_{l \in L} (C_l \times cost_{IP} + \sum_{f \in \Psi_l} cost_f) \quad (1)$$
  
s.t. 
$$\sum_{l:l_{src}=n} Y(l, \omega, \lambda) - \sum_{l:l_{dst}=n} Y(l, \omega, \lambda) = Traffic(\omega, n)$$
$$\forall \omega \in \Omega, \lambda \in \Lambda \qquad (2)$$
$$C_l \ge \sum_{\omega} Y(l, \omega, \lambda), \forall \lambda \in \Lambda \qquad (3)$$
$$\sum_{l \in \Delta_f} C_l \times \phi_{lf} \le S_f \qquad (4)$$
$$C_l \ge C_l^{min} \qquad (5)$$

Human-designed heuristics to trade optimality for tractability

Topology decomposition Topology transformation Failure selection

ILP solvers (Gurobi / CPLEX / SCIP)

Scalability issues: takes days or weeks

**Problem: Hard to find feasible solutions!** 

Two-stage approach





RL agent to learn topology (capacity variable  $C_l$ ) that leads to feasible solutions

Plan evaluator to give rewards based on feasibility.

- $\rightarrow$  Checking feasibility is much faster than minimizing cost
- → Faster way to check: **source aggregation** (SA) and **stateful failure checking**



Only search a local region of the RL solution to save ILP computation.



https://github.com/netx-repo/neuroplan

#### AutoCAT: Cache Side-Channel Attack



For different processes, their memory spaces are separate.
 Different processes share the same system cache → Security issues

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[M. Luo\*, W. Xiong\*, et al, AutoCAT: Reinforcement Learning for Automated Exploration of Cache Timing-Channel Attacks]



#### **Direct-Mapped** cache



Attacker accesses a0..a3



Victim accesses *secret address* v5



Attacker accesses a0, cache hit, fast memory access

Attacker accesses a1, cache miss, slow memory access
 → Victim's secret address must be v1 or v5.

## AutoCAT: RL to discover strategies automatically

- Discover novel form of side-channel attack is hard
  - Require expert knowledge with time commitment
  - New architecture / platforms emerge
  - Existing domain knowledge may lead to local optimal solutions, need "creativity"

#### Why not use Reinforcement Learning?

## AutoCAT: RL to discover strategies automatically

No	Cache config.		Attacker& victim config.			Expected attacks	Example Attack found by AutoCAT				
1.0.	Type <sup>†</sup>	Ways	Sets	Victim	Attack	Flush	Possible	Attack sequence (p indicates prefetch)	Attack		
		used		addr	addr	inst	attacks <sup>‡</sup>		Category		
1	DM	1	4	0-3	4-7	no	PP	$5 \rightarrow 4 \rightarrow 7 \rightarrow v \rightarrow 5 \rightarrow 7 \rightarrow 4 \rightarrow g$	PP		
2	DM+PFnextline	1	4	0-3	4-7	no	PP	$6(p7) \rightarrow 4(p5) \rightarrow v \rightarrow 4(p5) \rightarrow 5(p6) \rightarrow g$	PP		
3	DM	1	4	0-3	0-3	yes	FR	$\dots \rightarrow f1 \rightarrow v \rightarrow 1 \rightarrow f0 \rightarrow v \rightarrow f2 \rightarrow v \rightarrow 2 \rightarrow f3 \rightarrow 0 \rightarrow g$	FR		
4	DM	1	4	0-3	0-7	no	ER, PP	$\dots \rightarrow 3 \rightarrow 7 \rightarrow 4 \rightarrow 6 \rightarrow v \rightarrow 3 \rightarrow 0 \rightarrow 6 \rightarrow 4 \rightarrow g$	ER and PP		
5	FA	4	1	0/E	4-7	no	PP, LRU	$4 \rightarrow 6 \rightarrow 7 \rightarrow v \rightarrow 5 \rightarrow 4 \rightarrow g$	LRU		
6	FA	4	1	0/E	0-3	yes	FR, LRU	$0 \rightarrow 3 \rightarrow 1 \rightarrow 2 \rightarrow f0 \rightarrow 2 \rightarrow v \rightarrow 3 \rightarrow 0 \rightarrow g$	FR		
7	FA	4	1	0/E	0-7	no	ER, PP, LRU	$v \rightarrow 4 \rightarrow 1 \rightarrow 6 \rightarrow 7 \rightarrow v \rightarrow 1 \rightarrow v \rightarrow 5 \rightarrow 6 \rightarrow g$	LRU		
8	FA	4	1	0-3	0-3	yes	FR, LRU	$f3 \rightarrow f2 \rightarrow v \rightarrow 2 \rightarrow 3 \rightarrow f0 \rightarrow v \rightarrow 0 \rightarrow g$	FR		
9	FA	4	1	0-3	0-7	yes	FR, LRU	$f0 \rightarrow f2 \rightarrow f1 \rightarrow v \rightarrow 2 \rightarrow 1 \rightarrow 0 \rightarrow g$	FR		
10	DM	1	8	0-7	0.7	Vec	FD	$f2 \rightarrow v \rightarrow 2 \rightarrow f4 \rightarrow f0 \rightarrow v \rightarrow 0 \rightarrow 4 \rightarrow f3 \rightarrow f7 \rightarrow v \rightarrow 3 \rightarrow v \rightarrow$	FD		
10	DW	1	0	0-7	0-7	yes	IK	$7 \to f1 \to f6 \to v \to 6 \to 1 \to g$			
11	FA	8	1	0/E	0-7	yes	FR, LRU	$f0 \rightarrow v \rightarrow 0 \rightarrow g$	FR		
12	FA	8	1	0/E	0-15	no	ER, PP, LRU	$7 \rightarrow 11 \rightarrow 10 \rightarrow 5 \rightarrow 4 \rightarrow 2 \rightarrow 3 \rightarrow 1 \rightarrow v \rightarrow 0 \rightarrow g$	ER		
13	FA+PFnextline	8	1	0/E	0-15	no	ER, PP, LRU	$4 \ (p5) \rightarrow 9 \ (p10) \rightarrow 15 \ (p16) \rightarrow 2 \ (p3) \rightarrow v \rightarrow 0 \ (p1) \rightarrow g$	ER		
14	FA+PFstream	8	1	0/E	0-15	no	ER, PP, LRU	$15 \rightarrow 9 \rightarrow 8 \rightarrow 7(p6) \rightarrow 11 \rightarrow 6 \rightarrow 12 \rightarrow 14 \rightarrow v \rightarrow 0 \rightarrow g$	ER		

† FA: fully-associative, DM:direct-mapped, PFnextline: nextline prefetcher, PFstream: stream prefetcher. ‡ FR: flush+reload, ER: evict+reload, PP: prime+probe.

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[M. Luo\*, W. Xiong\*, et al, AutoCAT: Reinforcement Learning for Automated Exploration of Cache Timing-Channel Attacks] https://arxiv.org/abs/2208.08025

#### AutoCAT: Real hardware

#### System Cache (Fully associative)



#### AutoCAT: Real hardware



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will be replaced when attacker accesses address 4

#### AutoCAT: Real hardware

CDU	Cache	#Wove	Rep.	Victim	Attacker	Example attack sequence found by AutoCAT	Acouroca
Cru	level	# ways	Pol.	addr.	addr.	Example attack sequence found by AutoCAT	Accuracy
Xeon E5-2660v3	L1	8	PLRU	0/E	0-8	$2 \rightarrow 1 \rightarrow 5 \rightarrow 6 \rightarrow 4 \rightarrow 4 \rightarrow 7 \rightarrow 8 \rightarrow 4 \rightarrow 8 \rightarrow v \rightarrow 3 \rightarrow 4 \rightarrow v \rightarrow 0 \rightarrow g$	0.999
(Haswell)	L2	8	PLRU	0/E	0-8	$1 \rightarrow 8 \rightarrow 2 \rightarrow 3 \rightarrow 4 \rightarrow 7 \rightarrow 2 \rightarrow 5 \rightarrow 4 \rightarrow 2 \rightarrow 8 \rightarrow 6 \rightarrow \nu \rightarrow 6 \rightarrow 3 \rightarrow 6 \rightarrow 7 \rightarrow 1 \rightarrow g$	0.999
	L1	8	PLRU	0/E	0-8	$1 \rightarrow v \rightarrow 4 \rightarrow v \rightarrow 5 \rightarrow v \rightarrow 5 \rightarrow 5 \rightarrow 3 \rightarrow 8 \rightarrow 4 \rightarrow v \rightarrow 0 \rightarrow 2 \rightarrow 0 \rightarrow 1 \rightarrow v \rightarrow 8 \rightarrow 4 \rightarrow v \rightarrow g$	0.996
Core i7-6700	L2	4	N.O.D. <sup>‡</sup>	0/E	0-8	$0 \rightarrow 1 \rightarrow 7 \rightarrow 3 \rightarrow 6 \rightarrow 6 \rightarrow 6 \rightarrow 6 \rightarrow v \rightarrow 5 \rightarrow 0 \rightarrow 4 \rightarrow 1 \rightarrow 7 \rightarrow 5 \rightarrow g$	0.997
(SkyLake)	L3	4*	N.O.D.	0/E	0-8	$v \rightarrow v \rightarrow 4 \rightarrow 0 \rightarrow 5 \rightarrow 1 \rightarrow 1 \rightarrow 4 \rightarrow 2 \rightarrow 7 \rightarrow 3 \rightarrow 3 \rightarrow v \rightarrow v \rightarrow 3 \rightarrow 0 \rightarrow g$	1.0
	L3	8†	N.O.D.	0/E	0-8	$\dots \rightarrow 3 \rightarrow \nu \rightarrow 3 \rightarrow \nu \rightarrow 6 \rightarrow 7 \rightarrow 3 \rightarrow 3 \rightarrow 5 \rightarrow 1 \rightarrow 5 \rightarrow 1 \rightarrow 6 \rightarrow g$	0.966
Core i7-7700K	L3	4†	N.O.D.	0/E	0-8	$1 \rightarrow 2 \rightarrow 6 \rightarrow 6 \rightarrow 8 \rightarrow 8 \rightarrow 8 \rightarrow v \rightarrow 0 \rightarrow g$	1.0
(KabyLake)	L3	8†	N.O.D.	0/E	0-8	$\begin{array}{c} 7 \rightarrow 7 \rightarrow 3 \rightarrow 4 \rightarrow 6 \rightarrow 0 \rightarrow 2 \rightarrow 1 \rightarrow 6 \rightarrow 5 \rightarrow 3 \rightarrow 2 \rightarrow v \rightarrow 5 \rightarrow 4 \rightarrow 1 \rightarrow 2 \rightarrow 8 \rightarrow v \rightarrow 8 \rightarrow 7 \rightarrow 6 \rightarrow 6 \rightarrow 3 \rightarrow 3 \rightarrow 4 \rightarrow g \end{array}$	0.991

RL finds long sequence of memory access patterns to setup the cache state properly, before trigger to victim

### AutoCAT: RL to learn attacker to bypass defenders

Against Rule-based defender (autocorrelation)



## AutoCAT: RL to learn attacker to bypass defenders

Against ML-based defender

Attackers	Bit rate (guess/step)	Attack accuracy	SVM detection rate
Textbook	0.1625	1	1
RL_baseline	0.228	0.990	0.907
RL_SVM	0.150	0.964	0.021

#### AutoCAT: RL method and backbone

#### • Method to use

- Proximal Policy Optimization (PPO)
- Takes several hours to find good policies.
- Backbone
  - Transformers work much better than MLP, which is surprising.
  - Transformer may pick up the promising memory access sequences efficiently, given initial random explorations.

#### AutoCAT: Future works

- Generalizable policies across different system settings
  - Memory-Cache mapping (e.g., Direct Mapping, Set-Associative)
  - Cache perfetch/replacement strategies
  - Context-switch among processes
  - The presence of normal programs / defenders
- Game settings
  - Partially observable game between attackers and defenders.
  - Learn better defender as well.

## Part II: Learning Representation of State Space



#### Representation Learning in RL





What the current AI sees



What human players really see

#### Denoised MDP



GOAL: Letting in as much sunlight as possible







[T. Wang, et al, Denoised MDPs: Learning World Models Better Than The World Itself, ICML 2022]

#### Denoised MDP



(a) Transition without useful structure. *s* may contain any type of information.

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(c) Transition that factorizes out uncontrollable y and reward-irrelevant z.

**Controllability:** Uncontrollable factor dynamics are independent with other factors and actions (and only optionally additively affects rewards so the the set of optimal policies stay the same).

**Reward Relevance:** Reward-irrelevant factor does not affect any other factor or reward. Equivalently, if MDP transition can be represented as <u>below right</u>, latent *y* is **uncontrollable**, latent *z* is **reward-irrelevant**.

**Objective:** Minimizing reconstruction error with the designed state/action/reward structure

#### Experiments on (extended) RoboDesk

Original RoboDesk Env





## Experiments on (extended) RoboDesk



## DMC policy optimization with learned representation

	Mo	odel-based	1	Model-free						
	Policy Learning	g: Backprop	via Dynamics	Policy Learning: SAC (Latent-Space)			DBC	PI-SAC	CURL	State-Space SAC
	Denoised MDP	TIA	Dreamer	Denoised MDP	TIA	Dreamer	(No Aug.)		(Use Aug.)	(Upper Bound)
Noiseless	801.4 ± 96.6	769.7 ± 97.1	<b>848.6</b> ± 137.1	587.1 ± 98.7	480.2 ± 125.5	575.4 ± 146.2	297.4 ± 72.5	$246.4 \pm 56.6$	417.3 ± 183.2	910.3 ± 28.2
Video Background	<b>597.7</b> ± 117.8	407.1 ± 225.4	$227.8 \pm 102.7$	309.8 ± 153.0	318.1 ± 123.7	$188.7 \pm 78.2$	$188.0 \pm 67.4$	$131.7 \pm 20.1$	$478.0 \pm 113.5$	$910.3 \pm 28.2$
Video Background + Noisy Sensor	563.1 ± 143.0	261.2 ± 200.4	212.4 ± 89.7	<b>288.2</b> ± 123.4	197.3 ± 124.2	218.2 ± 58.1	79.9 ± 36.0	152.5 ± 12.6	354.3 ± 119.9	$919.8 \pm 100.7$
Video Background + Camera Jittering	<b>254.1</b> ± 114.2	151.7 ± 160.5	98.6 ± 27.7	186.8 ± 47.7	126.5 ± 125.6	105.2 ± 33.8	68.0 ± 38.4	91.6 ± 7.6	<b>390.4</b> ± 64.9	910.3 ± 28.2

Project page: <a href="https://ssnl.github.io/denoised\_mdp/">https://ssnl.github.io/denoised\_mdp/</a>

#### Representation Learning in RL



#### Is the temporal nature a **blessing** or a **curse**?

## Planning In a Trajectory Latent Space

**Trajectory Transformer** 



Single-Step Model

## Transformer as sequential modeling for Reinforcement Learning



Figure from [M. Janner et al, Trajectory Transformer, NeurIPS'21] <u>https://trajectory-transformer.github.io/</u> Accurate long-term prediction Quadratic time complexity



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[Z. Jiang, et al, Offline Reinforcement Learning with a Scalable Trajectory Generative Model]

## Trajectory Autoencoding Planner (TAP)

#### **Training**



1. Use VQ-VAE latent code to encode temporal segments

2. Train transformer on top of latent code



Planning Criterion =

- 1. Sample latent codes from prior model
- 2. Do beam search for the planning

#### Scale to Higher Dimensionality





**Increase Dimensionality** 

20

decision latency



TAP scales better both in terms of decision latency and the performance in D4RL







# Strong Performance in Adroit D4RL (robotic hand control)

#### high state and action dimensionality

Dataset	Environment	BC	CQL	IQL	MOPO	<b>Opt-MOPO</b>	TT	TAP (Ours)
Human	Pen	34.4	37.5	71.5	6.2	19.0	36.4	<b>76.5</b> ±8.5
Human	Hammer	1.5	4.4	1.4	0.2	0.5	0.8	$1.4 \pm 0.1$
Human	Door	0.5	9.9	4.3		_	0.1	$8.8 \pm 1.1$
Human	Relocate	0.0	0.2	0.1	_	_	0.0	$0.2 \pm 0.1$
Cloned	Pen	56.9	39.2	37.3	6.2	23.0	11.4	<b>57.4</b> ±8.7
Cloned	Hammer	0.8	2.1	2.1	0.2	5.2	0.5	$1.2 \pm 0.1$
Cloned	Door	-0.1	0.4	1.6	—	—	-0.1	<b>11.7</b> ±1.5
Cloned	Relocate	-0.1	-0.1	-0.2	_	_	-0.1	$-0.2\pm0.0$
Expert	Pen	85.1	107.0	_	15.1	50.6	72.0	$127.4 \pm 7.7$
Expert	Hammer	125.6	86.7	_	6.2	23.3	15.5	$127.6 \pm 1.7$
Expert	Door	34.9	101.5	—	—	—	94.1	$104.8 \pm 0.8$
Expert	Relocate	101.3	95.0	_	_	_	10.3	$105.8 \pm 2.7$
Mean (w	ithout Expert)	11.7	11.7	14.8	_		6.1	19.6
Mean	(all settings)	36.7	40.3	_	-	-	20.1	51.9





## Part III: Learning Design of State/Action Space

#### Predefined Action Space



Fixed action space =  $R^{361}$ 



[B. Zoph and Q. Le, Neural Architecture Search with Reinforcement Learning, 2016]

#### Predefined Action Space



#### What is a Good Representation for MDP itself?



If useful actions only happen after **50** binary moves, then we will waste our efforts in this **2**<sup>50</sup> possibilities.

#### Different Representation matters

Depth = {1, 2, 3, 4, 5} Channels = {32, 64} KernelSize = {3x3, 5x5}

1364 networks.

#samples

**Goal:** Find the network with the best accuracy using fewest trials.

#### **Representation of action space**

Sequential = { add a layer, set K, set C }

Global = { Set depth, set all K, set all C }



#### Different Partition $\rightarrow$ Different Value Distribution



## Why Predefined Action Space?



$$\frac{\text{Optimization problems}}{\min_{x} f(x)}$$

We only care the final solution

We don't care how we get it.

#### Learning to Partition --- How it works?





#### Learning to Partition



#### Learning to Partition





bad region, sample less!





#### Pros 🙄:

Rule out a lot of regions so that the sampling can be more efficient.

**Cons ::** The best solution can be in "bad" regions.

bad region, sample less!

## Latent Space Monte Carlo Tree Search (LaMCTS)

(a) Learn the action space.

(b) Search using learned action space until a fixed #rollouts are used.





[L. Wang, R. Fonseca, Y. Tian, Learning Search Space Partition for Black-box Optimization using Monte Carlo Tree Search, NeurIPS 2020] [L. Wang, S. Xie, T. Li, R. Fonseca, Y. Tian, Sample-Efficient Neural Architecture Search by Learning Action Space, TPAMI 2021]

#### Code is public now!



#### https://github.com/facebookresearch/LaMCTS

Both 3<sup>rd</sup> and 8<sup>th</sup> teams in NeurIPS 2020 Black-box optimization competition use our method!



## Open Domain

ImageNet	Model	FLOPs	Params	top1 / top5 err
(mobile setting	NASNet-A (Zoph et al. (2018))	564M	5.3 M	26.0 / 8.4
	NASNet-B (Zoph et al. (2018))	488M	5.3 M	27.2 / 8.7
FIOP < 6001VI)	NASNet-C (Zoph et al. (2018))	558M	4.9 M	27.5/9.0
	AmoebaNet-A (Real et al. (2018))	555M	5.1 M	25.5 / 8.0
	AmoebaNet-B (Real et al. (2018))	555M	5.3 M	26.0 / 8.5
	AmoebaNet-C (Real et al. (2018))	570M	6.4 M	24.3 / 7.6
	PNASNet-5 (Liu et al. (2018a))	588M	5.1 M	25.8 / 8.1
	DARTS (Liu et al. (2018b))	574M	4.7 M	26.7 / 8.7
	FBNet-C (Wu et al. (2018))	375M	5.5 M	25.1 / -
	RandWire-WS (Xie et al. (2019))	583M	5.6 M	25.3 / 7.8
	BayesNAS (Zhou et al. (2019))	-	3.9 M	26.5 / 8.9
	LaNet	570M	5.1 M	25.0 / 7.7

# La-MCTS as a meta method $x^* = \arg \min_{x \in \Omega} f(x)$



## Molecule Design

### Latent representation learned from unlabeled molecule dataset (1.8M molecules)



QED: a synthetic measure of drug-likeness (easy property)
DRD2: a measure of binding affinity to a human dopamine receptor
HIV: the potential inhibition probability for HIV
SARS: the potential inhibition probability for COVID-19

#### Code is available



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[K. Yang, T. Zhang, ... Y. Tian, Learning Space Partitions for Path Planning, NeurIPS 2021]

#### Multi-Objective Optimization (LaMOO)



Compute Dominant Number o(x)

$$o_{t,j}(\mathbf{x}) := \sum_{\mathbf{x}_i \in D_{t,j}} \mathbb{I}[\mathbf{x} \prec_{\mathbf{f}} \mathbf{x}_i, \ \mathbf{x} \neq \mathbf{x}_i]$$

 $o(\mathbf{x})$  can be computed in  $O(n \log n)$ 

Learn a space partition to separate good / bad regions



[Y. Zhao, ..., Y. Tian, Multi-objective Optimization by Learning Space Partitions, ICLR'22]

#### Molecule Design (32 dimensional input)



**GSK3β, JNK3:** biological targets **SA:** a standard measure of synthetic accessibility **QED:** a synthetic measure of drug-likeness **SARS:** the potential inhibition probability for COVID-19

#### Future Work

Design of State/Action Space



State Representation

**Search Heuristics** 

